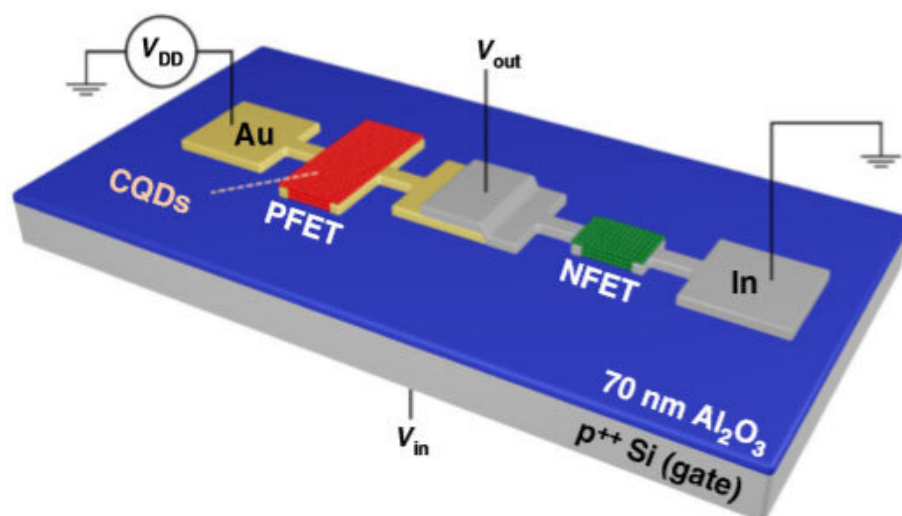


Breakthrough quantum-dot transistors create a flexible alternative to conventional electronics

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By depositing gold (Au) and Indium (In) contacts, researchers create two crucial types of quantum dot transistors on the same substrate, opening the door to a host of innovative electronics. Credit: Los Alamos National Laboratory

Researchers at Los Alamos National Laboratory and their collaborators from the University of California, Irvine have created fundamental electronic building blocks out of tiny structures known as quantum dots and used them to assemble functional logic circuits. The innovation promises a cheaper and manufacturing-friendly approach to complex electronic devices that can be fabricated in a chemistry laboratory via simple, solution-based techniques, and offer long-sought components for a host of innovative devices.

"Potential applications of the new approach to [electronic devices](#) based on non-toxic [quantum dots](#) include printable circuits, flexible displays, lab-on-a-chip diagnostics, wearable devices, medical testing, smart implants, and biometrics," said Victor Klimov, a physicist specializing in [semiconductor nanocrystals](#) at Los Alamos and lead author on a paper announcing the new results in the October 19 issue of *Nature Communications*.

For decades, microelectronics has relied on extra-high purity silicon processed in a specially created clean-room environment. Recently, silicon-based microelectronics has been challenged by several alternative technologies that allow for fabricating complex electronic circuits outside a clean room, via inexpensive, readily accessible chemical techniques. Colloidal semiconductor nanoparticles made with chemistry methods in much less stringent environments are one such emerging technology. Due to their [small size](#) and [unique properties](#) directly controlled by quantum mechanics, these particles are dubbed quantum dots.

A colloidal quantum dot consists of a semiconductor core covered with organic molecules. As a result of this hybrid nature, they combine the advantages of well-understood traditional semiconductors with the chemical versatility of molecular systems. These properties are attractive for realizing new types of flexible electronic circuits that could be

printed onto virtually any surface including plastic, paper, and even human skin. This capability could benefit numerous areas including consumer electronics, security, digital signage and medical diagnostics.

A key element of electronic circuitry is a transistor that acts as a switch of electrical current activated by applied voltage. Usually transistors come in pairs of n- and p-type devices that control flows of negative and positive electrical charges, respectively. Such pairs of complementary transistors are the cornerstone of the modern CMOS ([complementary metal oxide semiconductor](#)) technology, which enables microprocessors, memory chips, image sensors and other electronic devices.

The first quantum dot transistors were demonstrated almost two decades ago. However, integrating complementary n- and p-type devices within the same quantum dot layer remained a long-standing challenge. In addition, most of the efforts in this area have focused on nanocrystals based on lead and cadmium. These elements are highly [toxic heavy metals](#), which greatly limits practical utility of the demonstrated devices.

The team of Los Alamos researchers and their collaborators from the University of California, Irvine have demonstrated that by using copper indium selenide (CuInSe₂) quantum dots devoid of heavy metals they were able to address both the problem of toxicity and simultaneously achieve straightforward integration of n- and p-transistors in the same quantum dot layer. As a proof of practical utility of the developed approach, they created functional circuits that performed logical operations.

The innovation that Klimov and colleagues are presenting in their new paper allows them to define p- and n-type transistors by applying two different types of metal contacts (gold and indium, respectively). They completed the devices by depositing a common quantum dot layer on top of the pre-patterned contacts. "This approach permits straightforward

integration of an arbitrary number of complementary p- and n-type transistors into the same quantum dot layer prepared as a continuous, un-patterned film via standard spin-coating," said Klimov.

More information: Hyeong Jin Yun et al, Solution-processable integrated CMOS circuits based on colloidal CuInSe₂ quantum dots, *Nature Communications* (2020). [DOI: 10.1038/s41467-020-18932-5](https://doi.org/10.1038/s41467-020-18932-5)

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