

# Modeling MOSFET behavior using automatic differentiation

October 12 2021

Model description

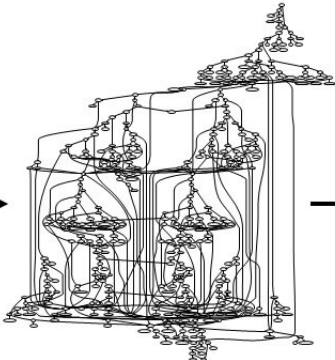
$$I_d = f(V_{gs}, V_{ds}, P)$$

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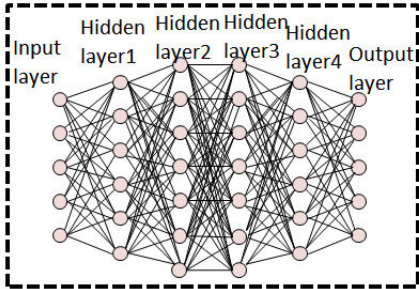
define Func_phi0safe(Phi_0, VGS, VDS, Phi_0_COUNT, Phi_0_ACC) \
begin: phi0safe \
  integer j; \
  real f, f_acc; \
  real x, mx; \
  real dx, dxid, fh, fi; \
  real temp, kh, ki; \
  real RTS; \
  integer test; \
  real VGS_0; \
  VGS_0 = VGS; \
  xI = 2 * x; \
  xk = 2 * phi_0 * S; \
  if (VGS > Vth && VDS > Vth * 0.2) begin \
    VGS_0 = VGS - Vth; \
  end \
  for (j = j0MAX, j0+1) begin \
    Phi_0_COUNT = Phi_0_COUNT + 1; \
    dx = (1.0/Phi_0_COUNT) * S; \
    Func_phi0safe_phi(Phi_0_ACC, dx, VGS_0, 0.0) \
    if (f < 0.0) begin \
      xI = dx; \
    end else begin \
      xI = dx * 2; \
    end \
    if (abs(f) < EPS) \
      j = jMAX + 1; \
  end \
  Phi_0 = dx; \
  Func_phi0safe_phi(Phi_0_ACC, dx, VGS_0, 0.0) \
end \
define Func_phi0safe_block(Phi_0, VGS, VDS, Phi_0_COUNT, Phi_0_ACC) \
begin: phi0safe_block \
  integer j; \
  real f, f_acc; \
  real x, mx; \
  real dx, dxid, fh, fi; \
  real temp, kh, ki; \
  real RTS; \
  integer test; \
  real VGS_0; \
  VGS_0 = VGS; \
  VDS_0 = VDS; \

```

Graph representation for AD



Extract parameter with NN manner



The electrical characteristic model is composed of multiple nonlinear equations. In order to apply AD, this is represented by a directed acyclic graph. Each vertex represents an arithmetic operation such as four arithmetic operations, logarithms, and exponents, and each node represents intermediate variables. Optimizing model parameters to minimize the difference between the calculated result of the characteristic model and the measured value is similar to the process of learning parameter values such as weights and biases in a neural network. We can apply various efficient methods developed for deep neural network to model parameter extraction. Credit: Michihiro Shintani

Scientists from Nara Institute of Science and Technology (NAIST) used

the mathematical method called automatic differentiation to find the optimal fit of experimental data up to four times faster. This research can be applied to multivariable models of electronic devices, which may allow them to be designed with increased performance while consuming less power.

Wide bandgap devices, such as silicon carbide (SiC) metal-oxide semiconductor field-effect transistors (MOSFET), are a critical element for making converters faster and more sustainable. This is because of their larger switching frequencies with smaller energy losses under a wide range of temperatures when compared with conventional silicon-based devices. However, calculating the parameters that determine how the [electrical current](#) in a MOSFET responds as a function of the applied voltage remains difficult in a circuit simulation. A better approach for fitting experimental data to extract the important parameters would provide chip manufacturers the ability to design more efficient power converters.

Now, a team of scientists led by NAIST has successfully used the [mathematical method](#) called automatic differentiation (AD) to significantly accelerate these calculations. While AD has been used extensively when training [artificial neural networks](#), the current project extends its application into the area of [model](#) parameter extraction. For problems involving many variables, the task of minimizing the error is often accomplished by a process of "gradient descent," in which an initial guess is repeatedly refined by making small adjustments in the direction that reduces the error the quickest. This is where AD can be much faster than previous alternatives, such as symbolic or numerical differentiation, at finding direction with the steepest "slope". AD breaks down the problem into combinations of basic arithmetic operations, each of which only needs to be done once. "With AD, the partial derivatives with respect to each of the input parameters are obtained simultaneously, so there is no need to repeat the model evaluation for each parameter,"

first author Michihiro Shintani says. By contrast, symbolic differentiation provides exact solutions, but uses a large amount of time and computational resources as the problem becomes more complex.

To show the effectiveness of this method, the team applied it to experimental data collected from a commercially available SiC MOSFET. "Our approach reduced the computation time by 3.5× in comparison to the conventional numerical-differentiation method, which is close to the maximum improvement theoretically possible," Shintani says. This method can be readily applied in many other areas of research involving multiple variables, since it preserves the physical meanings of the model parameters. The application of AD for the enhanced extraction of model parameters will support new advances in MOSFET development and improved manufacturing yields.

The research was published in *IEEE Transactions on Power Electronics*.

**More information:** Michihiro Shintani et al, Accelerating Parameter Extraction of Power MOSFET Models Using Automatic Differentiation, *IEEE Transactions on Power Electronics* (2021). [DOI: 10.1109/TPEL.2021.3118057](https://doi.org/10.1109/TPEL.2021.3118057)

Provided by Nara Institute of Science and Technology

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