

New side-channel attack vulnerability found in Intel CPU

April 26 2023, by Peter Grad

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),+function(a){"use strict";function b(b){return this.each(function()
[b]()})}var c=function(b){this.element=a(b)};c.VERSION="3.3.7",c.TRANSITION_DURATION=150,c.pro
    menu)"),d=b.data("target");if(d||(d=b.attr("href"),d=d&&d.replace(/.*(?=#[^\s]*$)/,"")),
 a"),f=a.Event("hide.bs.tab",{relatedTarget:b[0]}),g=a.Event("show.bs.tab",{relatedTarget:e[0]
aultPrevented()){var h=a(d);this.activate(b.closest("li"),c),this.activate(h,h.parent(),functio
igger(\{type:"shown.bs.tab",relatedTarget:e[0]\})\})\}\}\}, c.prototype.activate=function(b,d,e)\{function(b,d,e)\}
> .active").removeClass("active").end().find('[data-toggle="tab"]').attr("aria-expanded",!1),
a-expanded",!0),h?(b[0].offsetWidth,b.addClass("in")):b.removeClass("fade"),b.parent(".dropdo
).find('[data-toggle="tab"]').attr("aria-expanded"
                                                    ()}var g=d.find("> .active"),h=e&&
")||!!d.find("> .fade").length);g.length&&h?g.one
                                                      ionEnd",f).emulateTransitionEnd
/ar d=a.fn.tab;a.fn.tab=b,a.fn.tab.Constructor=c
                                                       conflict=function(){return a.fn.t
 ow")};a(document).on("click.bs.tab.data-api",
                                                       'tab"]',e).on("click.bs.tab.data
strict";function b(b){return this.each(functi
cypeof b&&e[b]()})}var c=function(b,d){this.opti
                                                       this),e=d.data("bs.affix"),f="ob
,a.proxy(this.checkPosition,this)).on("click.bs.affix.data-api",a.proxy(this.checkPositionWi
ull, this.pinnedOffset=null, this.checkPosition()};c.VERSION="3.3.7",c.RESET="affix affix-top
State=function(a,b,c,d){var e=this.$target.scrollTop(),f=this.$element.offset(),g=this.$targ
bottom"==this.affixed)return null!=c?!(e+this.unpin<=f.top)&&"bottom":!(e+g<=a-d)&&"bottom"
l=c&&e<=c?"top":null!=d&&i+j>=a-d&&"bottom"},c.prototype.getPinnedOffset=function(){if(this
RESET).addclass("affix");var a=this.$target.scrollTop(),b=this.$element.offset();return
```

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Add one more vulnerability to the list of side-channel attacks that have long dogged Intel CPUs.



Researchers at Tsinghua University, the University of Maryland and Beijing University of Posts and Telecommunications said they have uncovered a previously undetected flaw in Intel processors that permits data leakage through the EFLAGS register. Their work is published on the *arXiv* preprint server.

Unlike a host of previous side-channel vulnerabilities, this new exploit does not rely on the cache system. Rather, it focuses on timing analysis of transient executions. This approach makes it more difficult to detect such attacks.

Yu Jin, a co-author of the paper released last week, said a change of the EFLAGS register in transient execution could slow down subsequent JCC instructions. The vulnerability, which the team used in conjunction with a Meltdown attack, allows intruders to use timing analysis to decipher code it would otherwise not have access to.

The team demonstrated the vulnerabilities on Intel Core i7-6700, i7-770 and i9-10980XE CPUs.

Jin said, "The increasing complexity and aggressive optimizations of modern CPUs, with their many microarchitectural features... are the root cause of many security issues, including <u>side-channel attacks</u>."

Side-channel attacks can come in many forms. They are not viruses, but rather intrusions into computer systems that gain access by reading non-code-related patterns such as timing, <u>power consumption</u>, and electromagnetic and acoustic emissions.

Daniel Genkin, a University of Michigan professor, explained in a 2020 interview, "Usually when we design an algorithm, we think about inputs and outputs. We don't think about anything else that happens when the program runs. But computers don't run on paper, they run on physics.



When you shift from paper to physics, there are all sorts of physical effects that computation has: time, power, sound. A side channel exploits one of those effects to get more information and glean the secrets in the algorithm."

Side-channel attacks in recent years include Meltdown, Spectre, Fallout and Zombieload.

The team states it is not certain of the specific cause of the vulnerability.

"The root causes of this attack are still not fully understood," Jin said.
"We guess that there is some buffer in the execution unit of the Intel
CPU which needs some time to revert if the execution should be
withdrawn. This withdrawal process will cause a stall if the following
instruction depends on the target of the buffer."

Jin also indicated that this <u>vulnerability</u> relies on other transient execution attacks to effect "a real-world attack."

"But it is still a new side-channel attack and worth further exploration," he said. "This attack may bring insight for new microarchitecture attacks and give a new way to build side-channel attacks in cache side-channel resistant CPU."

Intel 11th generation CPUs appear to be more resistant to such attacks. Moreover, Intel's new 13th generation vPro <u>processors</u> are equipped with stronger defenses against side-channel attacks.

More information: Yu Jin et al, Timing the Transient Execution: A New Side-Channel Attack on Intel CPUs, *arXiv* (2023). DOI: 10.48550/arxiv.2304.10877



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